

FIGURE 1

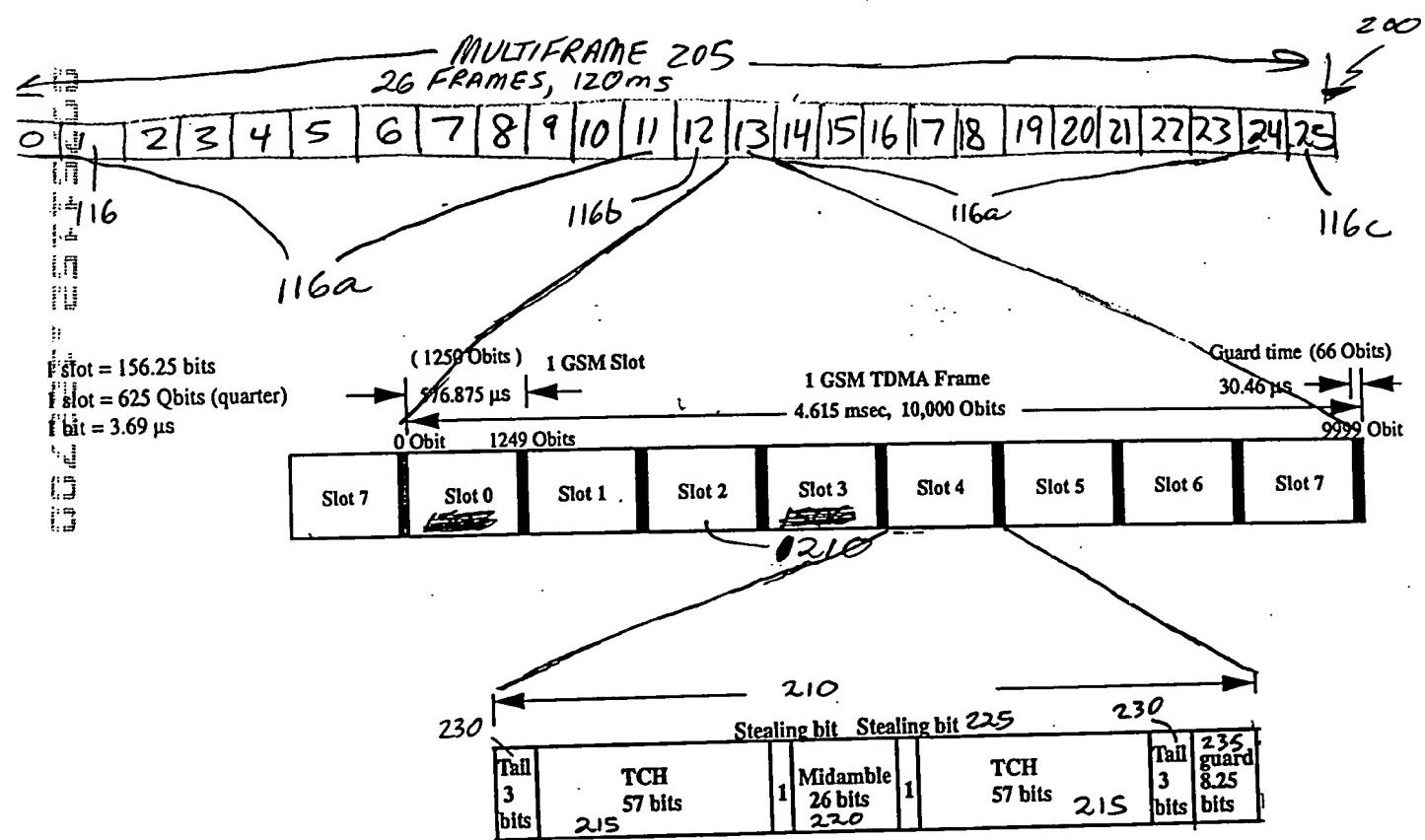
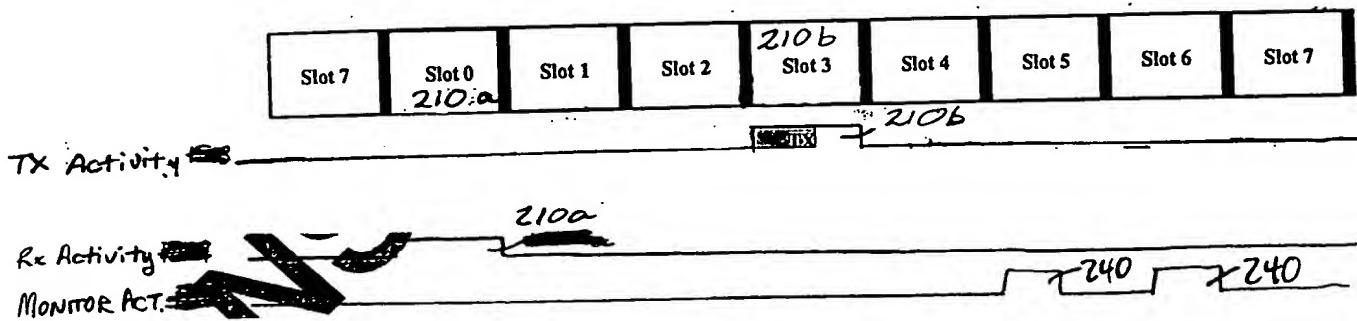


FIGURE 2A



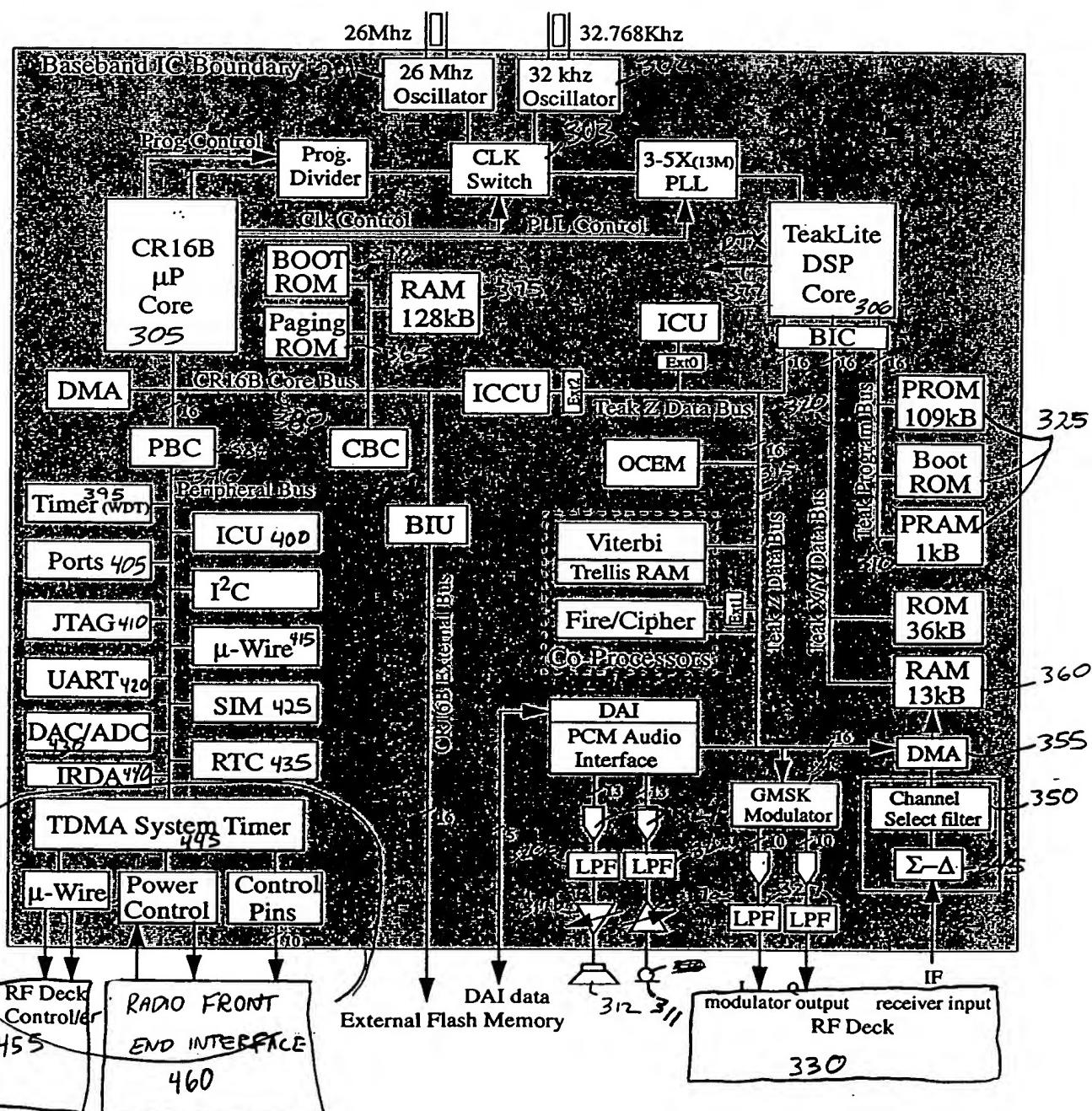
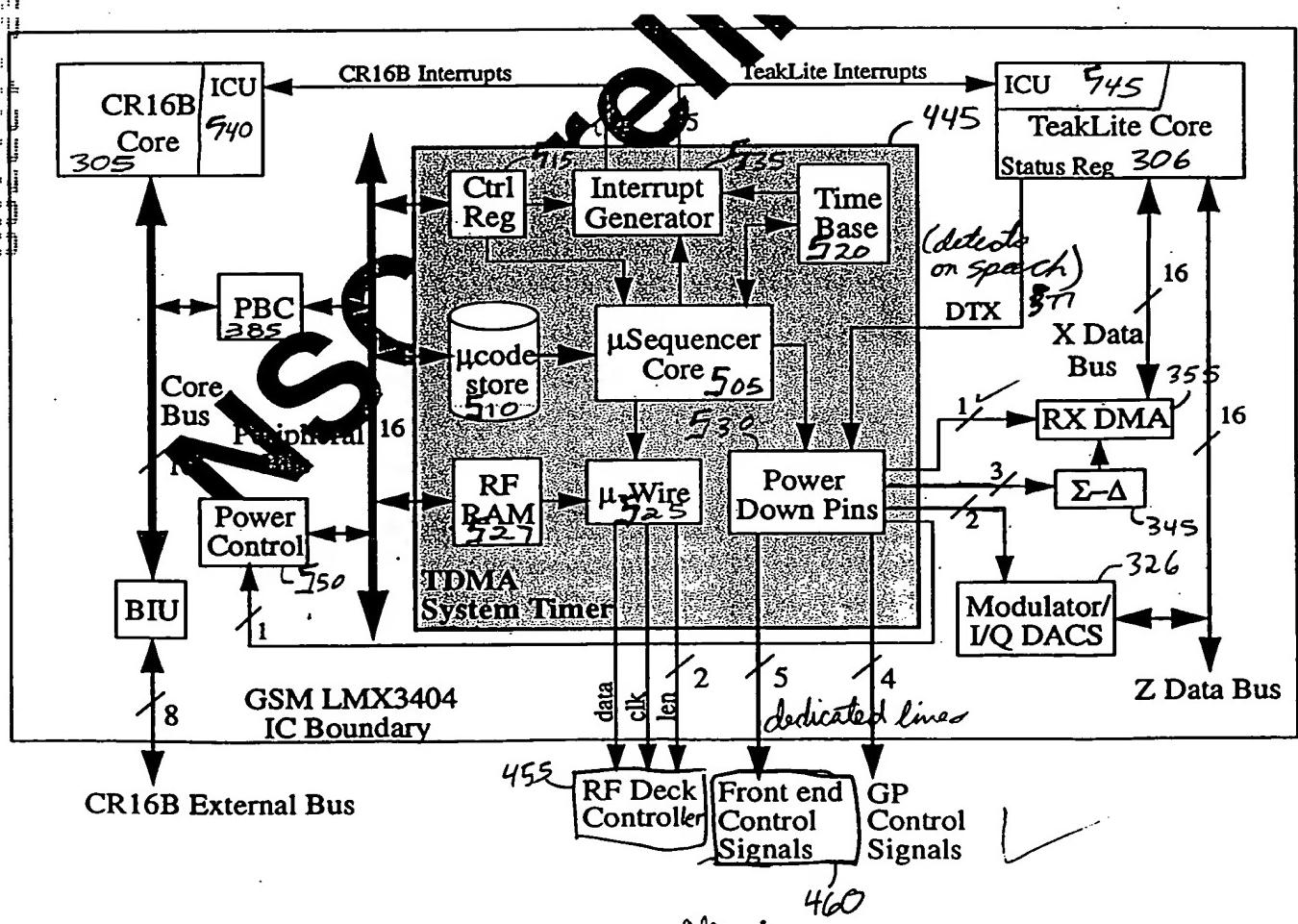


FIGURE 3



## FIGURE 4

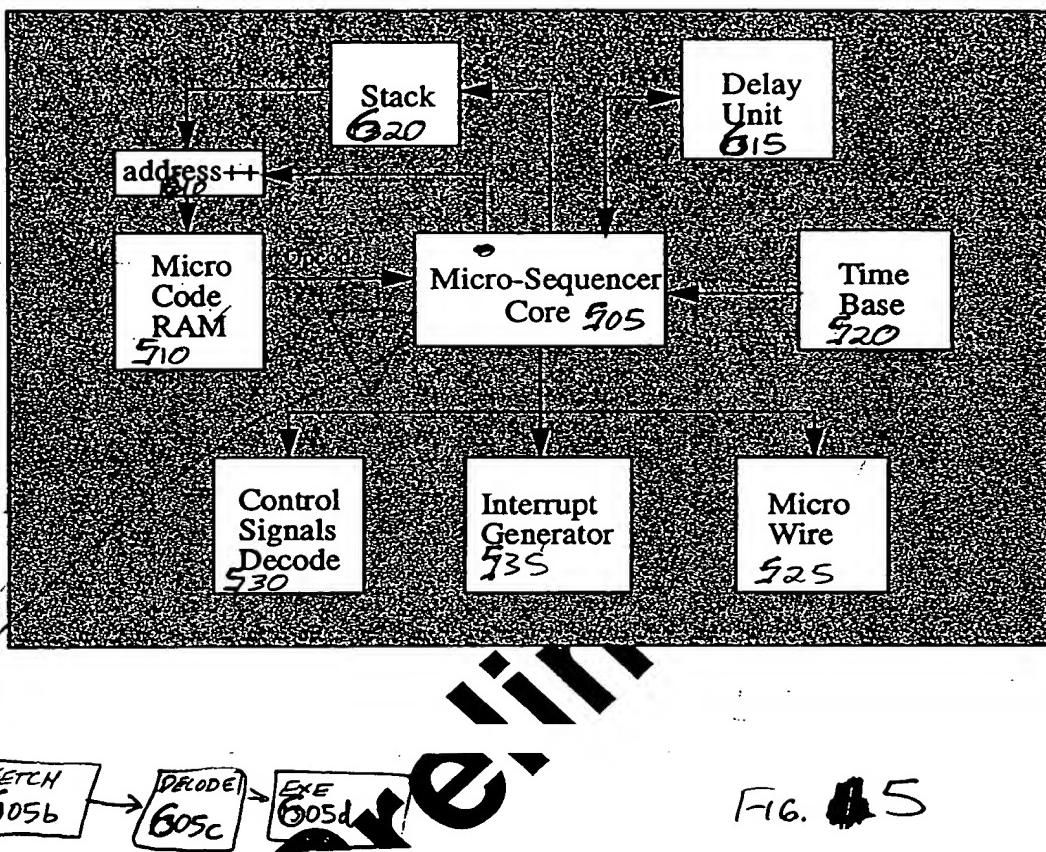


FIG. 45

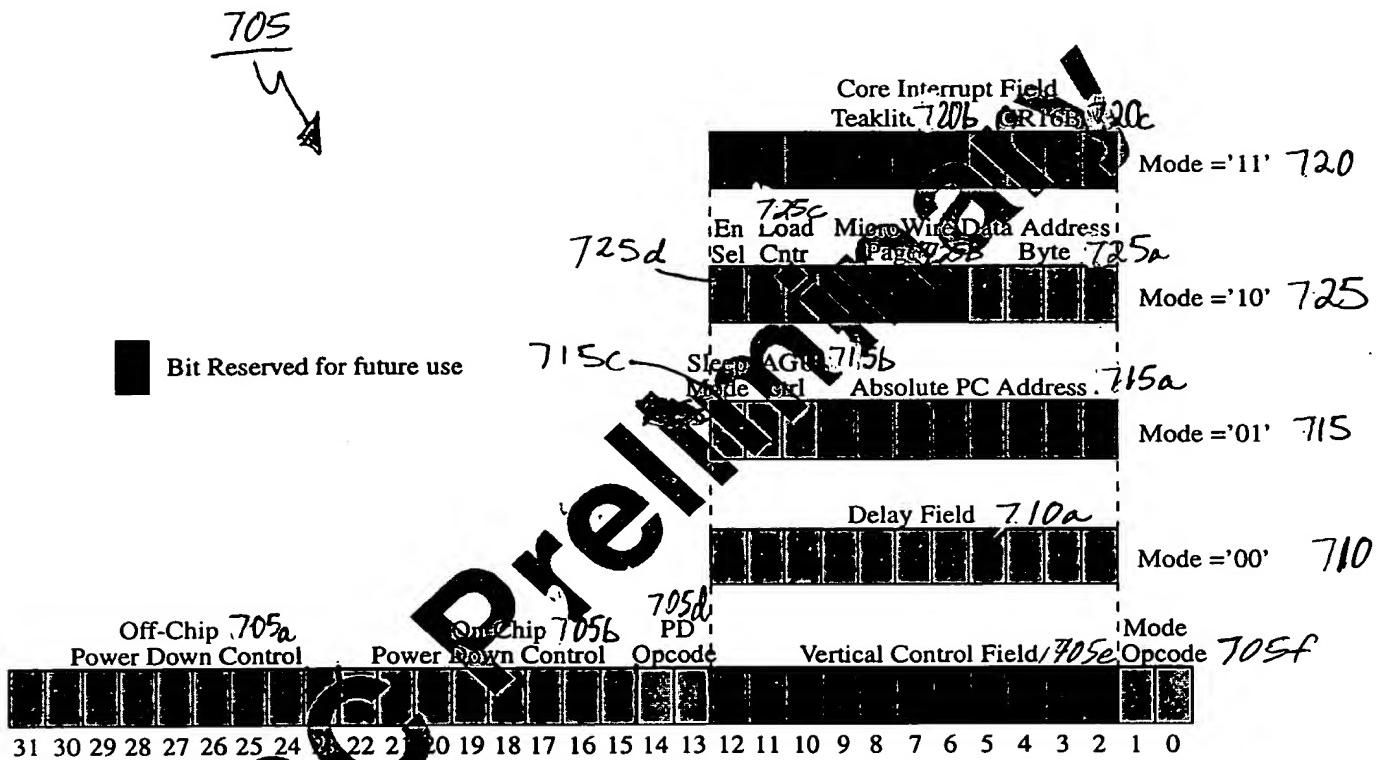


FIGURE 6.

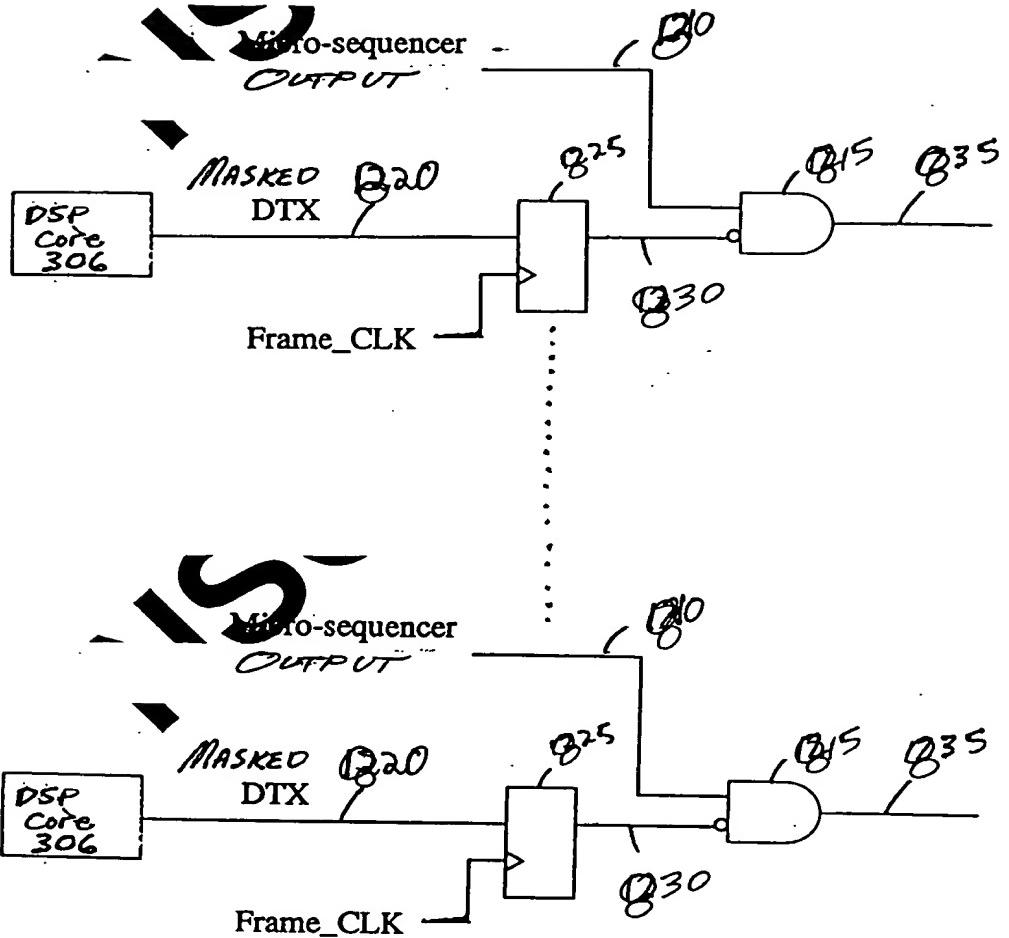


FIGURE ~~10~~ 7

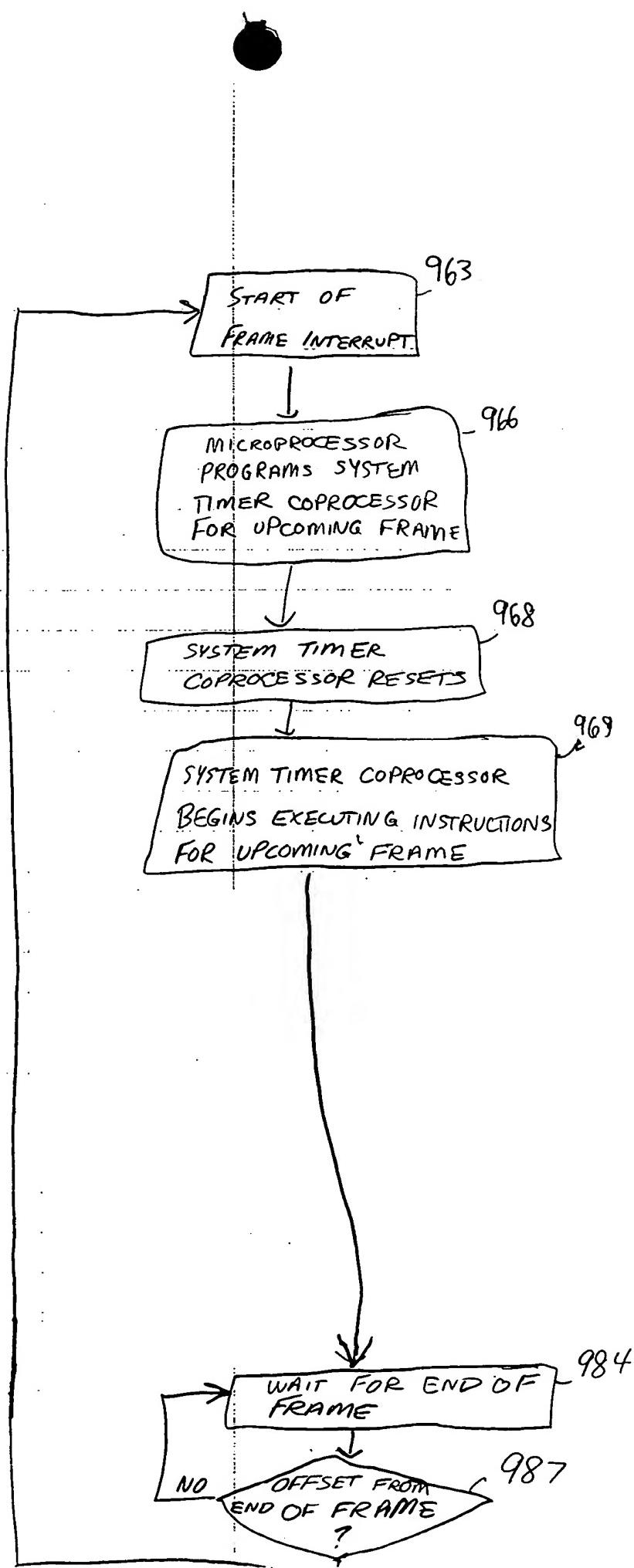
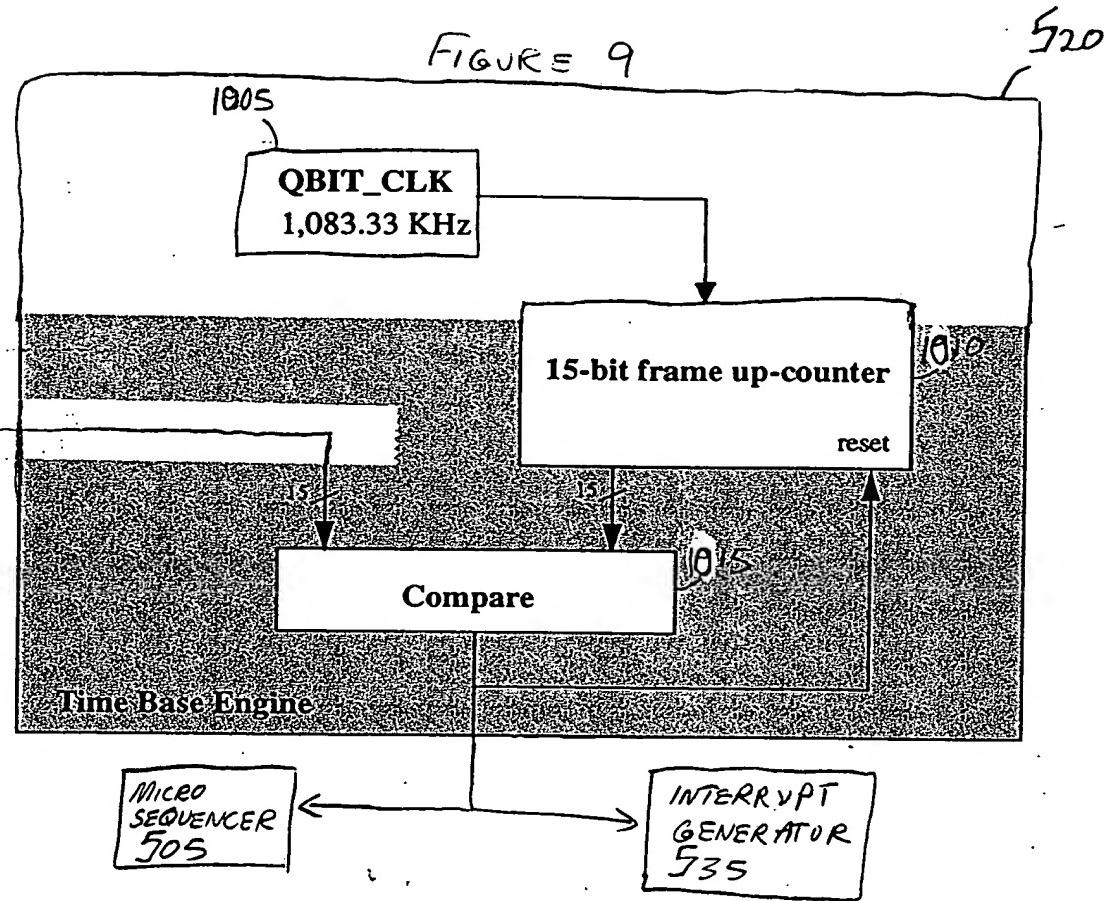


FIGURE 8



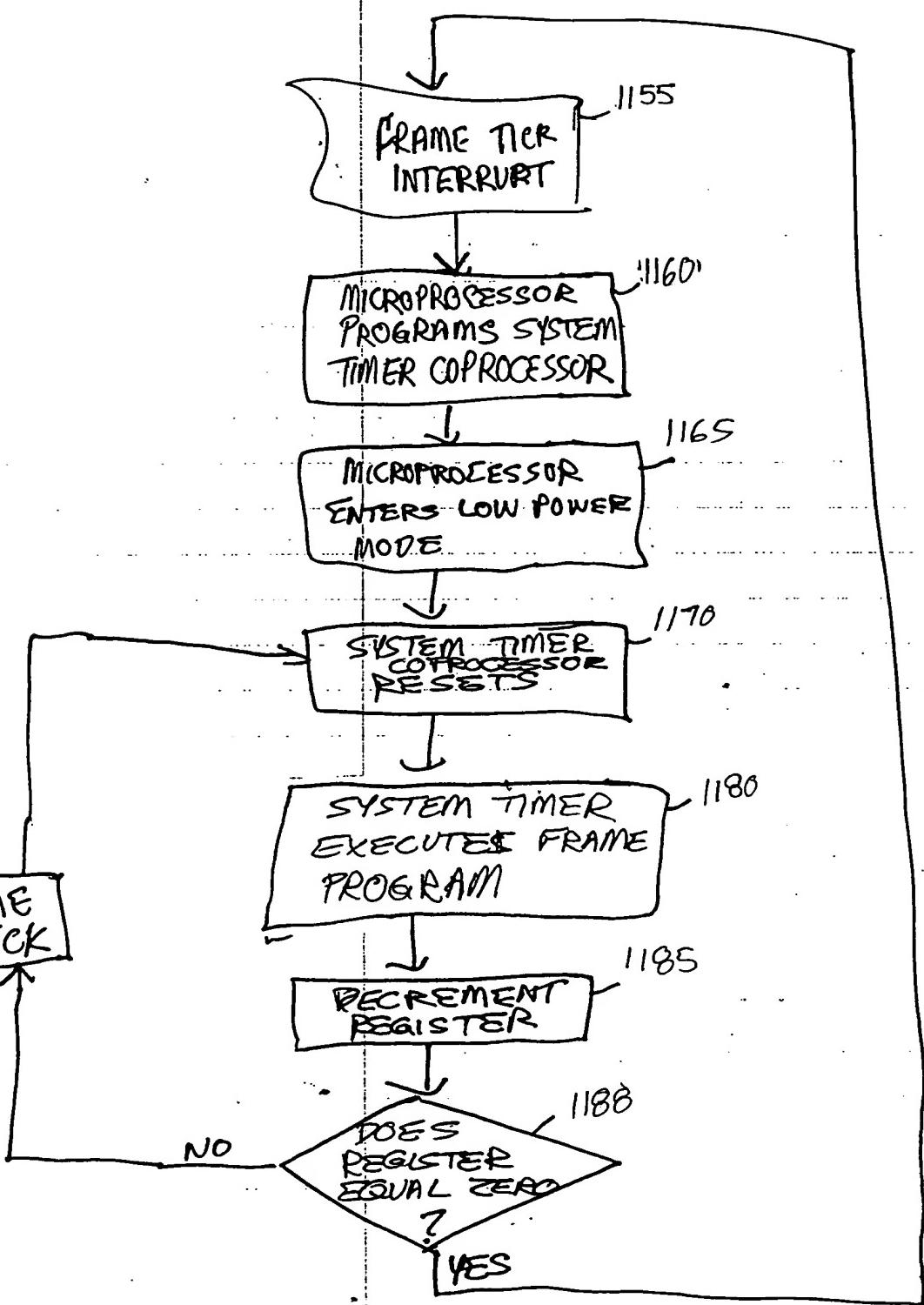


FIGURE ~~10~~ 10

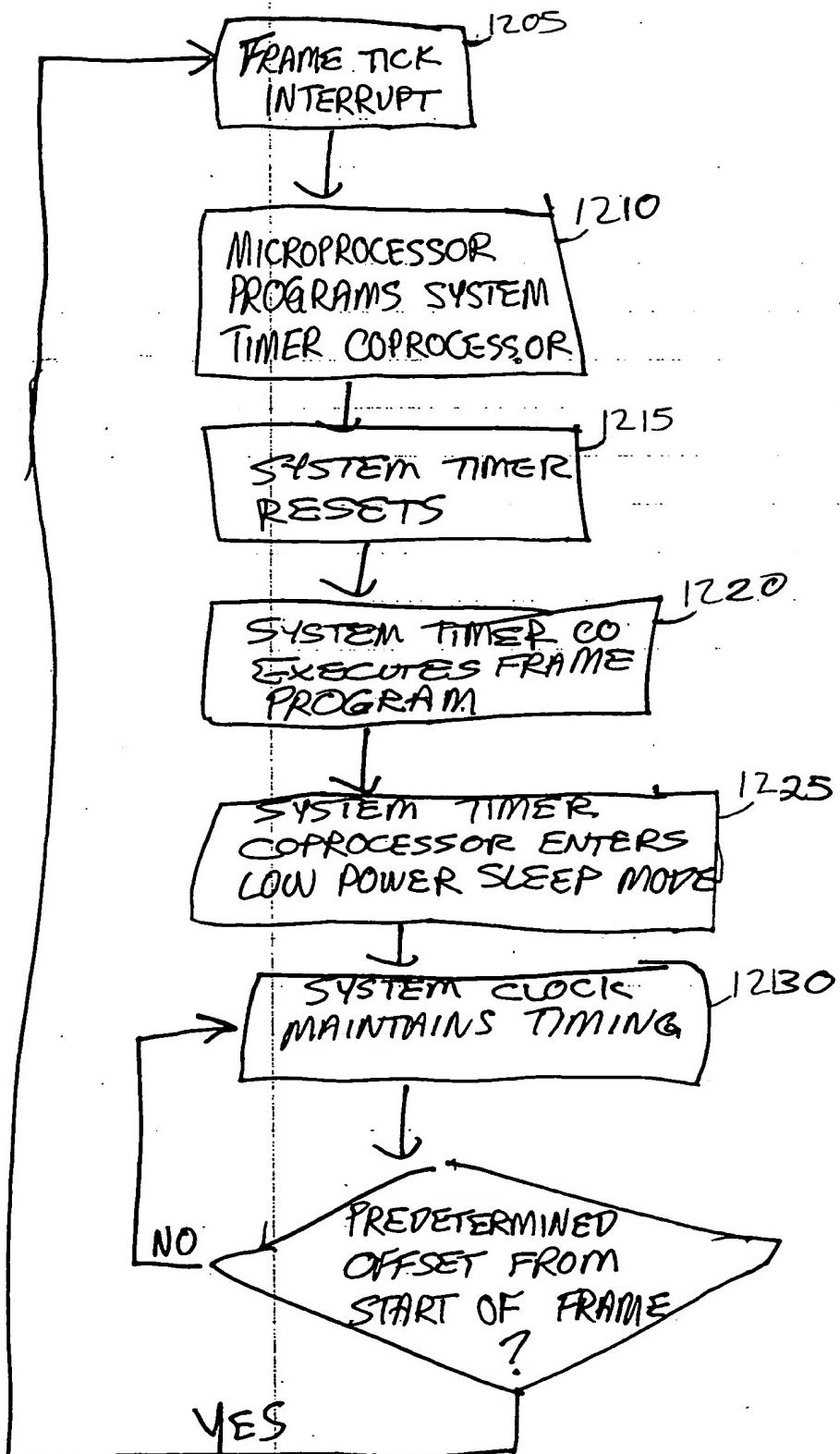
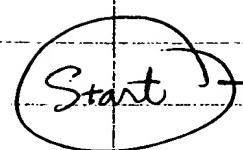


FIG. 11



MICROPROCESSOR  
MAKES DETERMINATION  
TO PLACE SYSTEM  
TIMER COPROCESSOR  
IN DEEP SLEEP MODE

1289

PROGRAM ~~WAKE~~ RF RAM,  
MICROCODE RAM, CONTROL  
REGISTERS WITH WAKEUP  
STATE

1290

DISABLE HIGH FREQUENCY -  
CLOCK FROM SYSTEM  
TIMER COPROCESSOR AND  
SWITCH TO LOW FREQUENCY  
CLOCK

1291

TIME EXPIRED?  
NO  
YES

1292

1293

SWITCH TO  
HIGH FREQUENCY

TRANSMIT  
ENABLE

1294



FIGURE 11B

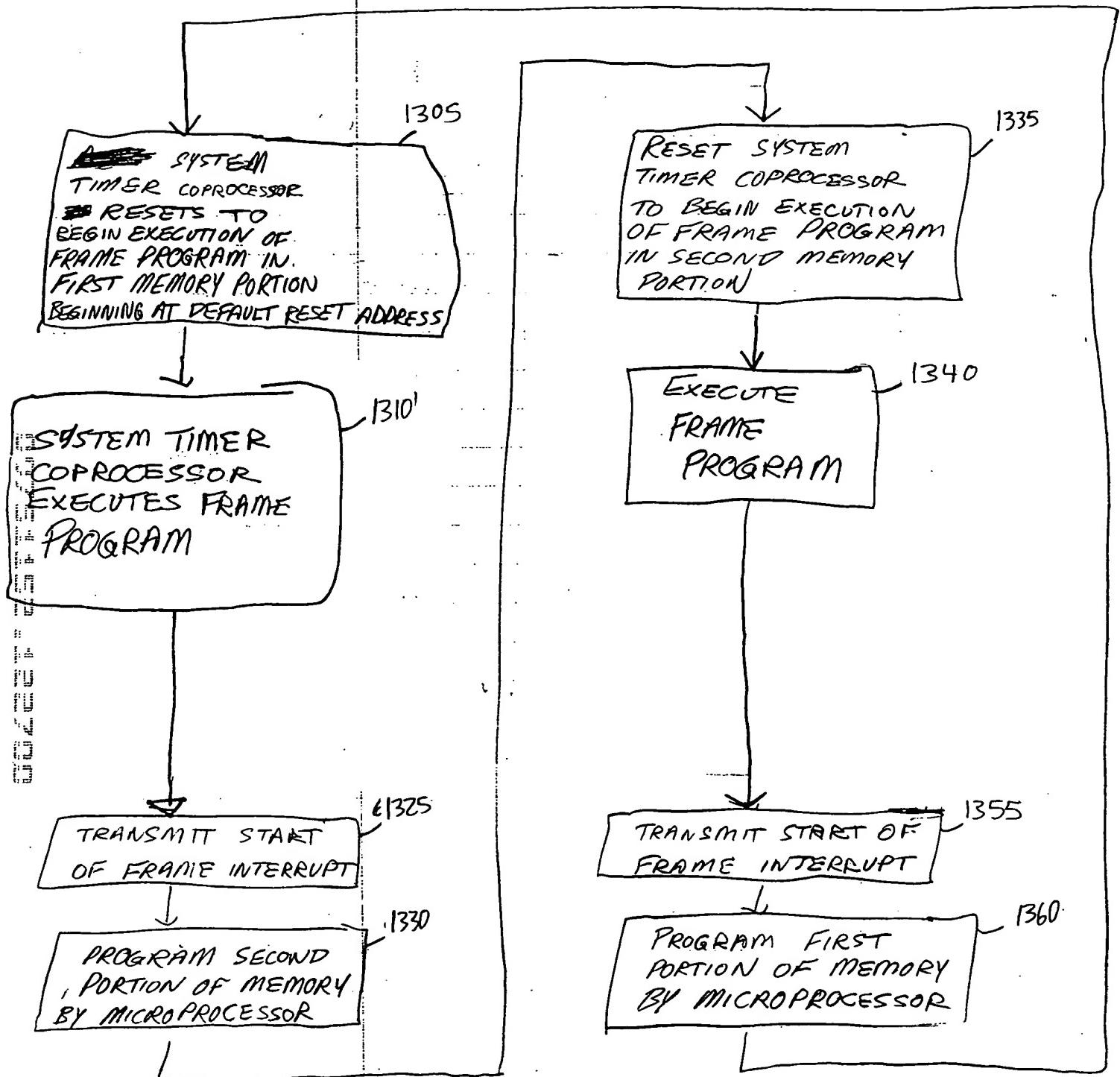


FIGURE ~~12~~ 12